

# Abstracts

## Review of SiGe process technology and its impact on RFIC design (2002 [RFIC])

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*A. Das, M. Huang, J. Mondal, D. Kaczman, C. Shurboff and S. Cosentino. "Review of SiGe process technology and its impact on RFIC design (2002 [RFIC])." 2002 Radio Frequency Integrated Circuits (RFIC) Symposium 02. (2002 [RFIC]): 325-328.*

Reviews recently published SiGe BiCMOS technologies for RFIC design. Performance and integration trends in SiGe HBTs are discussed. Performance of passive devices, such as an inductor, plays a key role in RF design. We review approaches to realize high Q inductor on a Si substrate. Finally, interaction of HBT performance with design is illustrated through LNA design.

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